

Section II:
AMENDMENT UNDER 37 CFR §1.121 to the
SPECIFICATION

In the Specification, please amend paragraph 0037 to read as follows:

[0037] Turning to Figure 3, a generalized architecture is presented including a central processing unit (31) ("CPU"), which is typically comprised of a microprocessor (32) associated with random access memory ("RAM") (34) and read-only memory ("ROM") (35) and other types of computer-readable media. Often, the CPU (31) is also provided with cache memory (33) and programmable FlashROM (36). The interface (37) between the microprocessor (32) and the various types of CPU memory is often referred to as a "local bus", but also may be a more generic or industry standard bus.